





PIN FUNCTIONS					
PIN #	NET NAME	FUNCTION	NOTES		
1	USBDM	USB DATA- NOTE			
2	USBDP	USB DATA +	NOTE 1,5		
3	Vin	POWER SUPPLY +			
4	RxD	UART receive	NOTE 1		
		(host to CV5)			
5	TxD	UART transmit	NOTE 1		
		(CV5 to host)	NOTE 1		
6	GPIO3	LOGIC LEVEL GPIO	NOTE 4		
7	GPIO1	LOGIC LEVEL GPIO	NOTE 3		
		(and pps input)			
8	GND	SIGNAL GROUND &			
		POWER SUPPLY RETURN			
9	GPIO2	LOGIC LEVEL GPIO	NOTE 4		
10	DISABLE	LOGIC LEVEL DISABLE			
		(OPEN OR LOW = ENABLE)			
MOUNTING	CHACCIC	CHASSIS GROUND	NOTE 2		
HOLES	CHASSIS	CUASSIS GROUND	INOTE 2		

NOTE 1: primary interface communications is via either uart or usb. unused interface pins may be left unconnected

NOTE 2: for best EMC performance, tie CHASSIS (i.e. the three mounting holes) to a local ground (e.g. pcb groundplane, airframe ground, etc.) CHASSIS and GND can be the same or different grounds, see ABSOLUTE MAXIMUM RATINGS table for limits

NOTE 3: currently implemented as input only for Pulse Per Second (PPS) timing input. leave unconnected or wire to GND if not used.

NOTE 4: future functionality; not currently implemented, these pins can be left unconnected, or wired to GND, or wired to a TTL/CMOS compatible device for possible future usage.

NOTE 5: CV5-10 does not make use of USB connections (pins 1/2) these pins may be left unconnected if USB is unused.

ABSOLUTE MAXIMUM RATINGS					
Parameter	Limit	Notes			
Vin to GND	±12V	NOTES 1,2			
Logic I/O to GND	-0.3V to Vin+0.3V AND not to exceed +5.4V	NOTES 2,3			
GPIO1 to GND	±12V	NOTE 2			
DISABLE to GND	±12V	NOTE 2			
GND to CHASSIS	±12V	NOTE 2			
Operating Temperature	-40°C to +85°C				
Mechanical Shock	500g				

NOTE 1: Power supply is PROTECTED against $\pm 12V$, but will not OPERATE over that full range. See INTERFACE OPERATING SPECIFICATIONS table for operational limits.

NOTE 2: Also protected against ESD and other high-voltage / low-energy transients.

NOTE 3: applied to pins: RxD, TxD, USBDM, USBDP, GPIO2, GPIO3

INTERFACE OPERATING SPECIFICATIONS						
Parameter	MIN	TYP	MAX	NOTES		
Power Supply Voltage (Vin)	+3.2V		+5.2V	Note 1		
Power Consumption		200mW				
DC Input Logic Low (Vil)			0.9V	NOTES 1,2,3		
DC Input Logic High (Vih)	2.1V			NOTES 1,2,3		
DC Output Logic Low (Vol)			0.4V	NOTES 1,2,4		
DC Output Logic High	2.6V			NOTES 1,2,4		
Disable Input Threshold	0.4V		1.6V	NOTES 1,5		

NOTE 1: All voltages are referenced to the GND pin (pin 8).

NOTE 2: Applies to pins: RxD, TxD, USBDM, USBDP, GPIO1, GPIO2, GPIO3.

NOTE 3: Nominal input impedance at RxD pin is 10Kohm to +3V. Nominal input impedance at GPIO1/2/3 pins is 40Kohm to GND. Nominal input impedance at USBDP pin is 1Kohm to +3V. Nominal input impedance at USBDM pin is 40Kohm to +3V.

NOTE 4: Applies when sourcing/sinking up to 6mA.

NOTE 5: Nominal input impedance at DISABLE pin is 1Mohm to GND when DISABLE voltage is between 0V and +5.6V. The DISABLE pin can be tied to GND or left unconnected when unused.

CORPORATION

Williston, VT 05495

CAGE

OXYZ9

OVERVIEW SHEET 4/4 ELECTRICAL

(LB.)

UNLESS OTHERWISE SPECIFIED

VOL (CU. IN.)

THE INFORMATION CONTAINED HEREIN	F.M.P. = FOR MANUFACTUR	RING PURPOSES
IS PROPRIETARY TO LORD	ALL DIMENSIONS ARE TO F	POINTS OF INTERSECTION
CORPORATION AND SHALL NOT BE	DIMENSIONING/TOLERANC	ING PER ASME Y14.5 - 2009
REPRODUCED OR DISCLOSED IN	ABBREVIATIONS, ACRONYI	MS, TERMINOLOGY PER TS-
	MACHINED SURFACES = 1	
DESIGN OR MANUFACTURE EXCEPT	BREAK ALL SHARP EDGES	.005in025in (0.1mm - 0.6
WHEN SUCH USER POSSESSES DIRECT,	AVERAGING OF DIAMET	ERS NOT PERMISSIBLE
	ALL DIMENSIONS ARE IN IN	
CORPORATION.	DIMENSIONS IN [] ARE IN I	MILLIMETERS
	CALC. WT.	3065-0242-ICD

PROPRIETARY NOTICE

SURFACE AREA (SQ. IN.)

RANCING PER ASME Y14.5 - 2009 **KYLE WERNER** BY ONYMS, TERMINOLOGY PER TS-027 CKD. BY OGES .005in - .025in (0.1mm - 0.6mm) ENGR. APPR. MFG. APPR. Q.E. APPR.

INTERFACE CONTROL DRAWING 3DM-CV5 (-10, -15, -25)

> DRAWING NO. **REV** SIZE 3065-0254-ICD

THIRD ANGLE PROJECTION

DWG TYPE

EC